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447-01-75
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PATENT
Attorney Docket No.: SONY-11300

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Technology Center 2600

In re Application of:

Quan A. Vu *et al.*

Serial No.: 09/249,642

Filed: February 12, 2000

For: **A METHOD OF AND APPARATUS
FOR GENERATING A PRECISE
FRAME RATE IN DIGITAL VIDEO
TRANSMISSION FROM A
COMPUTER SYSTEM TO A
DIGITAL VIDEO DEVICE**

) Group Art Unit: 2612

) Examiner: Wilson, J.

) **TRANSMITTAL LETTER**

) 260 Sheridan Avenue, Suite 420
) Palo Alto, CA 94306
) (650) 833-0160

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Enclosed please find a Supplemental Information Disclosure Statement and Form PTO-1449, including copies of the references contained thereon, for filing in the U.S. Patent and Trademark Office.

A \$240.00 check is also enclosed pursuant to 37 C.F.R. § 1.17(p) for filing this Supplemental Information Disclosure Statement after three months as set forth in 37 C.F.R. § 1.97(c).

The Commissioner is hereby authorized to charge any additional fee or credit overpayment to our Deposit Account No. 08-1275. **An originally executed duplicate of this transmittal is enclosed for this purpose.**

Respectfully submitted,
HAVERSTOCK & OWENS LLP

Dated: April 6, 2001

By: Jonathan O. Owens
Jonathan O. Owens
Reg. No.: 37,902

CERTIFICATE OF MAILING (37 CFR § 1.8(a))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Assistant Commissioner for Patents, Washington D.C. 20231

Attorneys for Applicants

- 1 -

HAVERSTOCK & OWENS LLP.
Date: 4/6/01 By: Jonathan O. Owens



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) **SUPPLEMENTAL INFORMATION**
) **DISCLOSURE STATEMENT**

) 260 Sheridan Avenue, Suite 420
) Palo Alto, California 94306
) (650)833-0160

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

The citations listed below, copies attached, may be material to the examination of the above-identified application, and are therefore submitted in compliance with the duty of disclosure defined in 37 C.F.R. §§ 1.56 and 1.97. The Examiner is requested to make these citations of official record in this application.

Applicants have become aware of the following printed publications which may be material to the examination of this application:

- U.S. Patent No. 4,713,702;
- U.S. Patent No. 5,128, 677;
- U.S. Patent No. 5,400,340;
- U.S. Patent No. 5,402,419;
- U.S. Patent No. 5,412,698;

CERTIFICATE OF MAILING (37 CFR § 1.8(a))

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HAVERSTOCK & OWENS LLP.
Date: 4/6/01 By: [Signature]

04/12/2001 JADD01 00000043 09249642

01 FC:126 180.00 OP

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Repln. Ref: 04/12/2001 JADD01 0012390900
DAB:081275 Name/Number:09249642
FC: 704 \$60.00 CR



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- U.S. Patent No. 5,434,860;
- U.S. Patent No. 5,444,709;
- U.S. Patent No. 5,493,570;
- U.S. Patent No. 5,546,389;
- U.S. Patent No. 5,594,734;
- U.S. Patent No. 5,596,581;
- U.S. Patent No. 5,598,415;
- U.S. Patent No. 5,602,841;
- U.S. Patent No. 5,621,725;
- U.S. Patent No. 5,668,811;
- U.S. Patent No. 5,710,773;
- U.S. Patent No. 5,781,599;
- European Publication No. 0 428 111 A2;
- Teener, M., "A Bus on a Diet - The Serial Bus Alternative: An Introduction to the P1394 High Performance Serial Bus," Apple Computer Inc, Santa Clara, CA, Doc. No. XP000340753, 1992 IEEE;
- "Architecture for High Performance Transparent Bridges," Software Patent Institute Database of Software Technologies, Record 10, Serial number TDB0792.0100, July, 1992;
- "Data Link Driver Program Design for the IBM Token Ring Network PC Adapter," Computer Communications, pp. 266-272, No. 5, October 12, 1989, GB; and
- Bloks, R.H.J, "The IEEE-1394 High Speed Serial Bus," Philips Journal of Research, pp. 209-216, Vol. 50, No. 1/2, 1996.



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This Supplemental Information Disclosure Statement under 37 C.F.R. §§ 1.56 and 1.97 is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that anyone or more of these citations constitutes prior art.

Respectfully submitted,

HAVERSTOCK & OWENS LLP

Dated: April 6, 2001

By: Jonathan O. Owens
Jonathan O. Owens
Reg. No.: 37,902

Attorneys for Applicants

FORM PTO-349 (Modified) APR 10 2001 (37 CFR § 1.986)		U.S. Department of Commerce Patent and Trademark Office		Attorney Docket No.: SONY-11300 Serial No.: 09/249,642		RECEIVED APR 16 2001 Technology Center 2600		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use Several Sheets If Necessary)				Applicant: Quan A. Vu <i>et al.</i>		Filing Date: February 12, 2000 Group Art Unit:		
U.S. PATENT DOCUMENTS								
Examiner Initials		Serial / Patent Number	Issue Date	Applicant / Patentee	Class	Subclass	Filing Date	
	AA	4,713,702	12/15/87	Ishihara <i>et al.</i>	360	79	9/5/84	
	AB	5,128,677	7/7/92	Donovan <i>et al.</i>	341	177	9/15/89	
	AC	5,400,340	3/21/95	Hillman <i>et al.</i>	370	105.3	3/4/93	
	AD	5,402,419	3/28/95	Osakabe <i>et al.</i>	370	85.1	12/17/93	
	AE	5,412,698	5/2/95	Van Brunt <i>et al.</i>	375	373	3/16/93	
	AF	5,434,860	7/18/95	Riddle	370	84	4/20/94	
	AG	5,444,709	8/22/95	Riddle	370	94.1	9/30/93	
	AH	5,493,570	2/20/96	Hillman <i>et al.</i>	370	105.3	3/15/95	
	AI	5,546,389	8/13/96	Wippenbeck <i>et al.</i>	370	60	7/12/94	
	AJ	5,594,734	1/14/97	Worsley <i>et al.</i>	370	395	11/1/93	
	AK	5,596,581	1/21/97	Saeijs <i>et al.</i>	370	394	6/3/94	
	AL	5,598,415	1/28/97	Nuber <i>et al.</i>	370	474	8/4/95	
	AM	5,602,841	2/11/97	Lebizay <i>et al.</i>	370	413	3/15/95	
	AN	5,621,725	4/15/97	Kawamura <i>et al.</i>	370	43	5/24/95	
	AO	5,668,811	9/16/97	Worsley <i>et al.</i>	370	424	6/7/95	
	AP	5,710,773	1/20/98	Shiga	370	512	7/14/95	
	AQ	5,781,599	7/14/98	Shiga	375	376	9/6/95	
FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS								
		Document Number	Publication Date	Country / Patent Office	Class	Subclass	Translation	
							Yes	No
	AR	0 428 111 A2	5/22/91	EP	G06F	13/28		
	AS							
	AT							
OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)								
	AU	Teener, M., "A Bus on a Diet - The Serial Bus Alternative: An Introduction to the P1394 High Performance Serial Bus," Apple Computer Inc, Santa Clara, CA, Doc. No. XP000340753, 1992 IEEE.						
	AV	"Architecture for High Performance Transparent Bridges," Software Patent Institute Database of Software Technologies, Record 10, Serial number TDB0792.0100, July, 1992.						
	AW	"Data Link Driver Program Design for the IBM Token Ring Network PC Adapter," Computer Communications, pp. 266-272, No. 5, October 12, 1989, GB.						
	AX	Blocs, R.H.J., "The IEEE-1394 High Speed Serial Bus," Philips Journal of Research, pp. 209-216, Vol. 50, No. 1/2, 1996.						
	AY							
Examiner:					Date Considered:			
EXAMINER: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								